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## APPLICANTS

Nobuyuki Sekikawa, Ashikagai-shi, JAPAN;

Masaaki Momen, Ojiya-shi, JAPAN;  
Wataru Andoh, Kamo-shi, JAPAN; Koichi Hirata, Sanjyo-shi, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*

This application is a DIV of 09/925,628 08/10/2001 PAT 6,690,070

## \*\* FOREIGN APPLICATIONS \*\*\*\*

JAPAN 2000-242617 08/10/2000

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 03/11/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	DRAWING 3	6	2
Verified and Acknowledged		Initials			

## ADDRESS

Barry E. Bretschneider  
 Morrison & Foerster LLP  
 Suite 300  
 20001650 Tysons Boulevard  
 McLean , VA  
 22102

## TITLE

Method of manufacturing insulated gate semiconductor device

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of
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